

Chart 8.1 CIRCUIT CONSTANTS OF SOME MAJOR PHASE CONTROLLED CIRCUITS†

(a) Name	(b) Connections	(c) Load voltage waveforms	(d) Peak forward voltage on SCR	Peak reverse voltage		(g) Max. load voltage ($\alpha = 0$) $E_D = \text{average d-c value}$ $E_D = \text{RMS a-c value}$
				(e) On SCR	(f) On diode	
(1) Half-wave resistive load			E	E	—	$E_D = \frac{E}{\pi}$ $E_D = \frac{E}{2}$
(2) Half-wave inductive load with free-wheeling rectifier			E	E	E	$E_D = \frac{E}{\pi}$
(3) Center-tap with resistive load, or inductive load with free-wheeling rectifier			E (possibly 2E if load open)	2E	E	$E_D = \frac{2E}{\pi}$
(4) Center-tap with resistive or inductive load—SCR in d-c circuit			E	0 ON CR ₁ ON CR ₂	$\frac{2E}{\pi}$	$E_D = \frac{2E}{\pi}$
(5) Center-tap with inductive load (no free-wheeling rectifier)			2E	2E	—	$E_D = \frac{2E}{\pi}$
(6) Single-phase bridge with 2 SCR's with common anode or cathode. Resistive load, or inductive load with free-wheeling rectifier			E	E (CR ₁ and CR ₂)	E	$E_D = \frac{2E}{\pi}$

† Assumes zero forward drop in semiconductors when conducting, and zero current when blocking; also zero a-c line and source reactance. Inductive d-c loads have pure d-c current.

Chart 8.1 (cont.)

(h) Load voltage vs trigger delay angle α	(i) Trigger angle full on to full off	Max. steady-state current in SCR		(m) Average amp	(n) Cond angle for max. current	(p) Ability to pumpback inductive load energy to supply line	(q) Fundamental frequency of load voltage ($f = \text{supply frequency}$)	(r) Notes and comments
		(k) Average amp	(l) Cond. angle					
$E_D = \frac{E}{2\pi}(1 + \cos \alpha)$ $E_D = \frac{E}{2\pi}(\pi - \alpha + \frac{1}{2} \sin 2\alpha)^{1/2}$	180°	$\frac{E}{\pi R}$	180°	—	—	—	f	
$E_D = \frac{E}{2\pi}(1 + \cos \alpha)$	180°	$\frac{E}{2\pi R}$ (load highly inductive)	180°	$0.54(\frac{E}{\pi R})$	210°	No	f	
$E_D = \frac{E}{\pi}(1 + \cos \alpha)$	180°	$\frac{E}{\pi R}$	180°	$0.26(\frac{2E}{\pi R})$	148°	No	2f	CR ₂ necessary when load is not purely resistive. Frequency limited by recovery characteristics of rectifiers and SCR.
$E_D = \frac{E}{\pi}(1 + \cos \alpha)$	180°	$\frac{2E}{\pi R}$	360°	$CR_1 = \frac{E}{\pi R}$ $CR_2 = 0.26(\frac{2E}{\pi R})$ with highly inductive load	180° 148°	No	2f	
$E_D = \frac{2E}{\pi} \cos \alpha$ (assuming continuous current in load)	180°	$\frac{E}{\pi R}$	180°	—	—	Yes	2f	
$E_D = \frac{E}{\pi}(1 + \cos \alpha)$	180°	$\frac{E}{\pi R}$	180°	$CR_1 = \frac{E}{\pi R}$ $CR_2 = 0.26(\frac{2E}{\pi R})$	180° 148°	No	2f	Without CR ₂ , SCR's may be unable to turn off on inductive load. Also, CR ₂ relieves SCR's from free-wheeling duty. See Sec. 5.5.

Chart 8.1 (cont.)

Circuit		(c)	(d)	Peak reverse voltage		(g)
(a)	(b)			(e)	(f)	
Name	Connections	Load voltage waveforms	Peak forward voltage on SCR	On SCR	On diode	Max. load voltage ($\alpha = 0$) $E_D = \text{average d-c value}$ $E_D = \text{RMS a-c value}$
(7) Single-phase bridge with 2 SCR's on common a-c line. Resistive or inductive load			E	E	E	$E_D = \frac{2E}{\pi}$
(8) Single-phase bridge with 4 SCR's and inductive load			E	E	—	$E_D = \frac{2E}{\pi}$
(9) Single-phase bridge with single SCR in d-c circuit. Resistive or inductive load			E	0	E (CR_1 and CR_2)	$E_D = \frac{2E}{\pi}$
(10) Three-phase half-wave with resistive load, or inductive load with free-wheeling rectifier			E (possibly $\sqrt{3}E$ if load open and if SCR's have high reverse currents)	$\sqrt{3}E$	E	$E_D = \frac{3\sqrt{3}E}{2\pi}$
(11) Three-phase half-wave with inductive load (no free-wheeling rectifier)			$\sqrt{3}E$	$\sqrt{3}E$	—	$E_D = \frac{3\sqrt{3}E}{2\pi}$
(12) Three-phase bridge with 3 SCR's. Resistive load, or inductive load with free-wheeling rectifier			$\sqrt{3}E$	$\sqrt{3}E$	$\sqrt{3}E$	$E_D = \frac{3\sqrt{3}E}{\pi}$
(13) Three-phase bridge with 6 SCR's. Resistive load, or inductive load with free-wheeling rectifier			$\sqrt{3}E$ (1.5E if SCR's shunted by resistance)	$\sqrt{3}E$	$\sqrt{3}E$	$E_D = \frac{3\sqrt{3}E}{\pi}$

Chart 8.1 (cont.)

(h)	(i)	Max. steady-state current in SCR		Max. steady-state current in diode rectifier		(p)	(q)	(r)
		(k)	(l)	(m)	(n)			
Load voltage vs trigger delay angle α	Trigger angle range full on to full off	Average amp	Cond. angle	Average amp	Cond. angle for max. current	Ability to pumpback inductive load energy to supply line	Fundamental frequency of load voltage ($f = \text{supply frequency}$)	Notes and comments
$E_D = \frac{E}{\pi}(1 + \cos \alpha)$	180°	$\frac{E}{\pi}$	180°	$\frac{E}{\pi}$	180°	No	$2f$	Diode rectifiers act as free-wheeling path, conduct $(\pi + \alpha)$ degrees with inductive load.
$E_D = \frac{2E}{\pi} \cos \alpha$ (assuming continuous current in load)	180°	$\frac{E}{\pi}$	180°	—	—	Yes	$2f$	With resistive load operation is same as circuit (7).
$E_D = \frac{E}{\pi}(1 + \cos \alpha)$	180°	$\frac{2E}{\pi}$	360°	$CR_1 = \frac{E}{\pi R}$ $CR_2 = 0.16 \left(\frac{2E}{\pi R} \right)$	180° 148°	No	$2f$	CR_2 necessary when load is not purely resistive. Frequency limited by recovery characteristics of rectifiers and SCR's.
$E_D = \frac{3\sqrt{3}E}{2\pi} \cos \alpha$ ($0 < \alpha < 30^\circ$)	150°	$\frac{\sqrt{3}E}{2\pi}$	120°	$0.16 \left(\frac{3\sqrt{3}E}{2\pi R} \right)$	134°	No	$3f$	
$E_D = \frac{3E}{2\pi} [1 + \cos (\alpha + 30^\circ)]$ ($30^\circ < \alpha < 150^\circ$)	150°	$\frac{\sqrt{3}E}{2\pi}$	120°	—	—	Yes	$3f$	
$E_D = \frac{3\sqrt{3}E}{2\pi} \cos \alpha$ (assuming continuous current in load)	180°	$\frac{\sqrt{3}E}{\pi}$	120°	$CR_1 = \frac{\sqrt{3}E}{\pi R}$ $CR_2 = 0.14 \left(\frac{3\sqrt{3}E}{\pi R} \right)$	120° 132°	No	$3f$	Without CR_2 , SCR's may be unable to turn off on inductive load. Also, CR_2 relieves SCR's from free-wheeling duty.
$E_D = \frac{3\sqrt{3}E}{2\pi} (1 + \cos \alpha)$	120°	$\frac{\sqrt{3}E}{\pi}$	120°	—	—	No	$6f$	SCR's require two gate signals 60° apart each cycle, alternately a gate signal duration $> 60^\circ$

Chart 8.1 (cont.)

Circuit		(c)	(d)	Peak reverse voltage		(g)
(a)	(b)			(e) On SCR	(f) On diode	
Name	Connections	Load voltage waveforms	Peak forward voltage on SCR	Peak reverse voltage		Max. load voltage ($\alpha = 0$)
				(e) On SCR	(f) On diode	
(14) Three-phase bridge with 6 SCR's with inductive load			$\sqrt{3} E$ (1.5E if SCR's shunted by resistance)	$\sqrt{3} E$	—	$E_D = \text{average d-c value}$ $E_o = \text{RMS a-c value}$
(15) Inverse parallel SCR's with resistive load			E	E	—	$E_D = \frac{E}{\sqrt{2}}$
(16) SCR inside bridge with a-c resistive load			E	0	E	$E_D = \frac{E}{\sqrt{2}}$

During the positive half-cycle of the supply voltage, the SCR anode is positive with respect to its cathode, and the gate can exert control over the SCR conduction characteristics as described in detail in Sec. 5.2. Until the gate is triggered by a proper positive signal from the trigger circuit, the SCR blocks the flow of load current in the forward direction. At some arbitrary delay angle α , a positive trigger signal is applied between the gate and cathode which initiates SCR current conduction. Immediately the full supply voltage, minus approximately one volt drop across the SCR, is applied to the load. With a zero reactance source and a purely resistive load, the current waveform after the SCR is triggered will be identical to the applied voltage wave, and of a magnitude dependent on the amplitude of the voltage and the value of load resistance R . As shown in Fig. 8.1(b), load current will flow until it is commutated by reversal of the supply voltage at $\omega t = \pi$. By controlling the trigger delay angle α with respect to the supply voltage by such means as described in Chap. 5 and later in this chapter, we may vary the phase relationship of the start of current flow to the supply voltage and control the load current from a maximum value down to zero—hence the term phase control.

Chart 8.1 (cont.)

(h)	(i)	Max. steady-state current in SCR		Max. steady-state current in diode rectifier		(p)	(q)	(r)
		(j) Trigger angle range full on to full off	(k) Average amp	(l) Cond. angle	(m) Average amp	(n) Cond. angle for max. current		
Load voltage vs trigger delay angle α								
$E_D = \frac{\sqrt{3}E}{\pi} \cos \alpha$ (assuming continuous current in load)	120°	120°	$\frac{\sqrt{3}E}{\pi R}$	120°	—	—	Yes	SCR's require two gate signals 60° apart each cycle, alternately a gate signal duration $> 60^\circ$
$E_D = \frac{E}{\sqrt{2}\pi} (\pi - \alpha + \frac{1}{2} \sin 2\alpha)^{1/2}$	180°	180°	$\frac{E}{\sqrt{2}\pi R}$ or $\frac{E}{\pi R}$	180°	—	—	—	With inductive load, load voltage and current depend on $\omega L/R$ as well as R and α
$E_D = \frac{E}{\sqrt{2}\pi} (\pi - \alpha + \frac{1}{2} \sin 2\alpha)^{1/2}$	180°	180°	$\frac{E}{\sqrt{2}\pi R}$ or $\frac{2E}{\pi R}$	360°	$\frac{E_D}{2\sqrt{2}\pi R}$ or $\frac{E}{\pi R}$	180°	—	Inductance in d-c circuit must be minimum. Frequency limit determined by recovery characteristics of rectifiers and SCR's. With inductive load, load voltage and current depend on $\omega L/R$ as well as R and α .

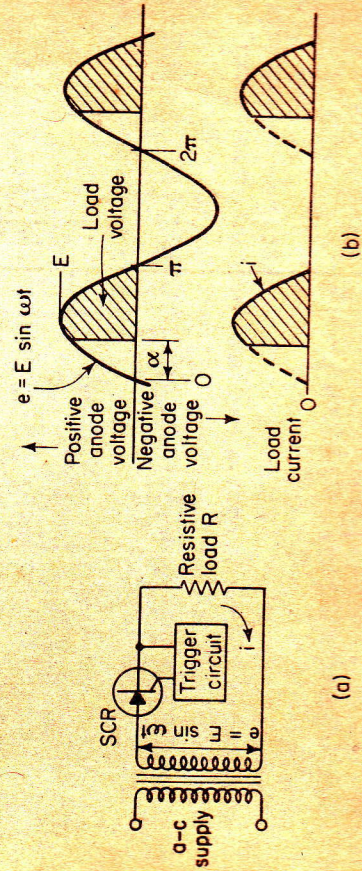


Fig. 8.1 Half-wave phase-controlled SCR with resistive load: