



**INSTITUT TEKNOLOGI BANDUNG**  
**SEKOLAH TEKNIK ELEKTRO DAN INFORMATIKA**  
**SCHOOL OF ELECTRICAL ENGINEERING AND INFORMATICS**  
Gedung Achmad Bakrie (Labtek VIII) Lt. 2, Jl. Ganesha 10, Bandung 40132, Telp.: 022-2502260, Fax: 022-2534222

**Transkrip Akademik Program Magister**  
**(Academic Transcript of Master Program)**

No : 010/K01.16.5.4/PP/2013

Nama : Nur Sakinah Asaad Program Studi : Teknik Elektro  
(Name) (Program of Study) (Electrical Engineering)  
NIM : 23209365 Jalur Pilihan : Mikroelektronika  
(Student Number) (Major) (Microelectronics)  
Tempat/Tanggal Lahir : Bangkok, 25 November 1982 Tahun Masuk : 2009  
(Place/Date of Birth) (Bangkok, November 25<sup>th</sup>, 1982) (Admission Year)

| KODE<br>(Code)             | MATAKULIAH<br>(Courses)                                                                                  | SKS<br>(Credit Hours) | NILAI<br>(Grade) |
|----------------------------|----------------------------------------------------------------------------------------------------------|-----------------------|------------------|
| EL5042                     | Perancangan IC Analog dan Mixed Signal (Analog and Mixed Signal IC Design)                               | 2                     | AB               |
| EL5045                     | Analisis & Perancangan IC Digital (Analysis & Design of Digital IC)                                      | 2                     | B                |
| EL5115                     | Pengujian dan Keterujian Sistem Digital (Digital System Testing & Testable Design)                       | 3                     | B                |
| EL5141                     | Nanoelektronika (Nanoelectronics)                                                                        | 2                     | A                |
| EL5142                     | Sistem Instrumentasi Cerdas (Intelligent Instrumentation Systems)                                        | 2                     | B                |
| EL5143                     | Metodologi Perancangan dan Analisis Sistem Embedded (Design Metodology and Analysis of Embedded Systems) | 2                     | B                |
| EL5144                     | VLSI untuk Pengolahan Sinyal Dijital (VLSI for Digital Signal Processing)                                | 2                     | AB               |
| EL5099                     | Metoda Penelitian (Research Methodology)                                                                 | 3                     | A                |
| EL6011                     | Sistem Embedded & Waktu Nyata (Embedded Systems & Real Time)                                             | 3                     | A                |
| EL6099                     | Tesis (Thesis)                                                                                           | 6                     | A                |
| EL6143                     | Proyek Perancangan Sistem Embedded (Project of Embedded Systems Design)                                  | 2                     | A                |
| EL6145                     | Topik Lanjut Teknik Elektronika (Advanced Topics on Electronics Engineering)                             | 2                     | A                |
| EL6210                     | Sistem Paralel (Parallel System)                                                                         | 3                     | A                |
| EL6242                     | Teknologi MEMS (MEMS Technology)                                                                         | 2                     | A                |
| EL6243                     | Sistem dan Teknologi Sensor (Sensor Systems and Technology)                                              | 2                     | B                |
| Jumlah SKS (Total Credits) |                                                                                                          | 38                    |                  |

Judul Tesis : Alur Perancangan Hardware Sistem Embedded Dengan Kode Framework VHDL Yang Di Generate Otomatis  
(Thesis Title) (Design Flow of Embedded System With VHDL Code Framework Generated Automatically)

Pembimbing (Advisor) : Arif Sasongko ST., M.Sc., Ph.D.

Telah Menyelesaikan Program Pendidikan Magister  
(Has Completed All Requirements for the Master Degree)

Tanggal Lulus Ujian Akhir : 18 Maret 2013  
(Date of Final Examination) (March 18<sup>th</sup>, 2013)

Tanggal Wisuda : 6 April 2013  
(Date of Graduation) (April 6<sup>th</sup>, 2013)

Nilai Rata-rata : 3.66  
(Grade Point Average)

Bandung, 8 April 2013

Ketua Program Magister Teknik Elektro  
Head of Electrical Engineering Master's Program

Ir. Yudi Satria Gondokaryono, MSEE., Ph.D.  
NIP. 19660604 19901 1 001

Dekan Sekolah Teknik Elektro dan Informatika  
Dean of the School of Electrical Engineering and Informatics

Prof. Dr. Ir. Suwarno, MT.  
NIP. 19651110 199001 1 001

Catatan (Note) : A = 4, B = 3, C = 2, AB = 3,5 BC = 2,5 (Minimum Passing Grade)