



July 04, 2016

**CERTIFICATE OF APPRECIATION**  
**on behalf of**  
**Mr. Andreas B. Vogel, Dipl. Ing.**  
**for his contribution to the Department of Electrical Engineering**  
**Hasanuddin University MAKASSAR 90245 INDONESIA**  
**2003-2006, 2007-2011 and 2012-2016**

To Whom It May Concern:

**Mr. Andreas B. Vogel, Dipl. Ing.** has decided to leave after completing his third term – with a total of not less than 11 (eleven) years - of tenure with our Department. I truly believe that he is leaving only on a temporary basis, yet he has asked that I prepare some sort of certificate to appreciate all his efforts during his stay with us in Makassar, INDONESIA all these years. I do so with great enthusiasm.

I first met with Mr. Vogel in 2003 when he and his wife were taking an intensive *Bahasa Indonesia* course in Bandung. I was at that time the chairman of the Department of Electrical Engineering at Hasanuddin University in Makassar, INDONESIA. Mr. Vogel showed his great interest to work with us in the Department after finishing his *Bahasa Indonesia* course. He has the very expertise that we really needed at that time, so we willingly accepted his offer to teach and to do research in our department.

Mr. Vogel has done much more than we expected. He developed new courses namely *VLSI System Design*, *FPGA System Design* and *Embedded System*. These development activities included developing the courses' syllabii, lecture notes, laboratory materials, examination problem-sets and classroom presentations. In doing so he worked closely with our staff, showing his ability to work together in cooperation with others who have different cultures and backgrounds. He also upgraded 2 (two) other courses namely *Integrated Circuits Technology* and *Electronic Systems Design* to become better courses in reflecting a realistic overview on latest technologies and current design methods. For these efforts, Mr. Vogel and his teaching team have been awarded a competitive Teaching Grant by an ADB-funded project for the Department's quality enhancement programs.

After a short leave in the period of 2006-2007, Mr. Vogel came back to resume his teaching position. In addition to his previous activities, he took more advanced tasks such as teaching the graduate courses, especially in the *VLSI System Design* and in the implementation of *Verilog HDL* using *FPGA technology*. He also participated in our research activities, including presenting a research paper in an International Conference held by our Department in 2008 and participating as a member of the steering committee in an international workshop on modern research methods in electrical engineering in 2009. He also chaired a local seminar on *VLSI Design* attended by both interested students and lecturers from the vicinity of Makassar area. Mr. Vogel was also enthusiastically involved in our main research themes, such as the robotic control system, the GPS navigation system and the traffic-light control system, all developed on the FPGA platform.

Mr. Vogel's third term with our department started in 2012. He continued his previous works of developing course materials both in the undergraduate and graduate programs in

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our department. In addition to the 5 (five) undergraduate courses he previously developed and taught, Mr. Vogel also helped to develop a fundamental course in our department *Digital Electronics*, and created a new elective course *VLSI System Implementation* as a follow-up of his previously established *VLSI System Design* course. Beside teaching, Mr. Vogel also continued his research and development (R&D) activities in VLSI Design, especially (but not limited to): (1) in developing laboratory complementary exercises for teaching *VLSI* and *Digital Electronics* courses, (2) in developing FPGA resources (this has made the department became a member of the XILINX university program who received the FPGA-SoC hardware units) and also (3) in an R&D project on modular system blocks for low cost embedded systems (e.g. digital oscilloscope) design. He continuously and consistently promotes the advancement of the FPGA technologies by mentoring and supervising his undergraduate and graduate students.

In special international events such as the IwoRMEE 2013 and the MICEEI 2014, Mr. Vogel participated by conducting a tutorial on "VLSI System Design" and delivered a speech entitled "Trends in FPGA Technology and Its Use in Next-Generation Designs" as a keynote speaker. He also conducted seminars on "VLSI System Design" in the surrounding academic communities and other institutions such as the State Polytechnic of Ujung Pandang (*PNUP*) and the Training Center for Vocational Skills (*BLK*). Recently, he submitted a JICA proposal to establish a Regional (East Indonesia) FPGA Training Center at the Center of Technology (COT) of the new Hasanuddin University Faculty of Engineering campus in Gowa. The approval of this proposal is still pending possibly due to the lack of funding.

In developing and teaching courses, Mr. Vogel always communicated in *Bahasa Indonesia* fluently with his students and colleagues. He also supervised many of our students to do their under-graduate final research and development projects, and guiding them to write the reports and theses in *Bahasa Indonesia*. Mr. Vogel has accomplished all his tasks perfectly with excellent results. I have been so thoroughly impressed by Mr. Vogel's competence and long experience in his field. He has shown a very high motivation to do whatever research and teaching programs he had planned to do, so that he also motivate others – his students and colleagues – to do the same. Our department is very grateful with his presence. We are all indebted to his expertise, high level of initiative, work ethics and over-all general behavior in relating to both colleagues and students, professionally and socially. He has been our living example of a professional academician.

Mr. Vogel has told me that after his return to Germany, he is planning to propose another period of stay in our Department, this time to do more involved research projects leading to a doctoral degree. I strongly recommended him to continue his study to pursue an advanced academic degree. I am really happy to hear that, so we are looking forward to seeing him again in our Department in the near future.

Should you wish any further information, please feel free to contact me, and I will be more than happy to respond to your inquiries.

With kindest regards,

**Dr. Ir. H. Rhiza S. Sadjad, M.S.E.E.**

Head of the Control System and Instrumentation Laboratory

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